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Complementary metal-oxide-semiconductor, also known as complementary-symmetry metal-oxide-semiconductor, is a type of metal-oxide-semiconductor field-effect transistor fabrication process that uses complementary and symmetrical pairs of p-type and n-type MOSFETs for logic functions. CMOS technology is used for constructing integrated circuit chips, including microprocessors, microcontrollers, memory chips, and other digital logic circuits. CMOS technology is also used for analog ...

CMOS – Wikipedia
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CMOS: Circuit Design, Layout, and Simulation can also be used with standard software packages used in academia and industry (Cadence, L-Edit, Magic, Mentor, etc.). It is useful as an advanced-level textbook or reference for engineers, engineering managers, layout designers, layout draftsmen, computer engineers, professors, and computer scientists.

CMOS CMOS CMOS IC Layout CMOS Analog and Mixed-Signal Circuit Design CMOS: MIXED-SIGNAL CIRCUIT DESIGN Design of CMOS Phase-Locked Loops CMOS, Circuit Design, Layout, and Simulation CMOS, Circuit Design, Layout, and Simulation Systematic Design of Analog CMOS Circuits Mixed-Signal Systems CMOS Logic Circuit Design Nano-CMOS Circuit and Physical Design Cmos Circuit Design Layout And Simulation CMOS analog circuit design CMOS VLSI Design : A circuits and systems perspective CMOS, TwoVolume Set CMOS Circuit Design – Analog, Digital, IC Layout Design of Analog CMOS Integrated Circuits Transient-Induced Latchup in CMOS Integrated Circuits CMOS Analog Integrated Circuits

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